

## Aldrin Claytus Vaz

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[LinkedIn](#) | [Scopus](#) | [ORCID](#) | [WOS](#) | [Google Scholar](#)



### WORK EXPERIENCE

Designation	Period	Name of the Organization
Assistant Professor – Grade II	July 2025 to Present	St Joseph Engineering College, Mangaluru
Assistant Professor	November 2021 to June 2025	Karavali Institute of Technology, Mangaluru
Assistant Professor	August 2015 to May 2016	Schneider Electric India Pvt. Ltd., Bengaluru
Project Trainee	June 2013 – July 2014	Manipal Institute of Computer Education, Udupi
Part-time teaching staff	July 2014 - July 2015 June 2016 – December 2016 May 2020, February 2021 September 2021	

### SUBJECTS HANDLED

#### Theory

Wireless & Cellular Communication  
Principles of Communication Systems  
ARM Processor & Microcontroller  
Sensors & Signal Conditioning  
Basic Electronics  
Introduction to Electronics Engineering  
Automotive Electronics  
Machine Learning  
Basics of Machine Learning

#### Lab

ARM Processor & Microcontroller  
Signals & Systems  
Computational Tools for Engineers  
Analog Electronics  
Computer Communication Network

#### COLLEGE LEVEL ASSIGNMENTS

Head of Research Centre, Dept. of ECE  
Member of Research & Facilities Group

#### DEPARTMENT LEVEL ASSIGNMENTS

Placement Coordinator  
NBA C2 Coordinator  
Coordinator of Student Technical Club “Samarthya”  
Research/Industry Internship coordinator (AY 2024-25)  
Summer Internship II coordinator (AY 2023-24)  
Project coordinator (AY 2022-23)  
LIC coordinator (AY 2022-23)

### CORE COMPETENCIES

- Problem-solving and analytical skills
- Proficient in MATLAB/SIMULINK, C, basics of Python, MP Lab IDE, Keil  $\mu$ Vision, ModelSim, Pspice.
- Competent in Coding Theory, Wireless Communication, Embedded Systems, Power & Digital Electronics.

## EDUCATION

Degree	Specialization	Year of Study	Name of the Institute/University	Result	Theses / Projects
Ph.D	Error Control Coding for Wireless Communication	Jan 2017 – Mar 2024	Manipal Institute of Technology / Manipal Academy of Higher Education	Awarded	<u>Performance Improvement of Visible Light Communication System using Advanced Coding Techniques</u>
M.Tech	Power Electronics Systems & Control	2012 – 2014	Manipal Institute of Technology / Manipal University	9.39/10 FCD	<u>Smart &amp; Intelligent Solar Charge Controller</u>
B.E	Electronics & Communication Engineering	2008 - 2012	The National Institute of Engineering, Mysuru / Visvesvaraya Technological University	9.20/10 FCD	<u>GSM-based notice board updation using microcontroller</u>

## Projects carried out at Schneider Electric Pvt. Ltd., Bengaluru

## Academic Projects carried out at Manipal Institute of Technology, MAHE, Manipal

## FUNDS & IPRs

- Received Rs 93,000/- seed money grant from SJEC, Mangaluru to work on Optimization of Visible Light Communication Framework for enhancing the performance of Vehicle-to-vehicle communication from SJEC, Mangaluru (AY 2023-24)
- Published an Indian Patent titled “A System and a Method for Filtering False Positives in Face Recognition Systems”

## OTHER DETAILS

- KSET 2024 Qualified | UGC-NET 2018 Qualified (Asst. Prof.) | GATE 2017 & 2012 Qualified
- NPTEL Domain Certification in Data Science
- Coursera Certifications
- Workshops | FDPs | STTPs | Webinars

## PUBLICATIONS

**Journals: 10 | Conferences: 18**

Date: 04/05/2026

Place: Udupi



(Aldrin C Vaz)