



SAVITHA J

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OBJECTIVE

Seeking career in the reputed organization having an environment that encourages continuous learning and provide exposure to new technologies so as to achieve professional growth along with the organization and I could utilize my skills in constituting effectively to the success of the organization..

EDUCATION

- **2011-2013** **NMAMIT, NITTE, VTU**
NITTE
 - M.Tech Microelectronics and Control Systems
 - 70.58% FCD
- **1998-2002** **AIT, CHIKMAGALUR**
Chikmagaluru
 - B.E Electrical & Electronics
 - Engineering 65.15% FC
- **1996-1998** **CANARA PRE UNIVERSITY COLLEGE**
Mangaluru
 - Class XII
 - 66.33% FC
- **1995-1996** **ST ANNS HIGH SCHOOL**
Mangaluru
 - Class X
 - 85.14% FCD

CERTIFICATIONS

- Qualified GATE- 2008 with 80.95 percentile score (All India Ranking 2877).
- NPTEL Course on Data Science for Engineers during Jan/March 2024 with a score of 68% .
- NPTEL Course on Python for Data Science during Jan/Feb 2024.with a score of 70% .
- NPTEL Course on Ethical Hacking during Jan/April 2023 with a score of 66% NPTEL
- Course on “C Based VLSI Design” during July/Oct 2022 with a score of 69%
- NPTEL Course on “Advanced Linear Continuous Control System, Applications in Matlab Programming and Simulink” during July/Sep 2019 with a score of 83%

PRESENTATIONS

“A comparative study on Multimodal Face Recognition using PCA, LDA, ICA and Sparse representation” , International Conference on emerging trends in Engineering, Nitte, India, May15-16, 2013.

EXPERIENCE

♦ 2023 - 2024 Mangaluru	ST JOSEPH ENGINEERING COLLEGE ♦ Assistant Professor
♦ 2006 - 2023 Mangaluru	SHREE DEVI INSTITUTE OF TECHNOLOGY ♦ Assistant Professor and HOD
♦ 2005 - 2006 Mangaluru	CANARA ENGINEERING COLLEGE ♦ Lecturer
♦ 2003 - 2005 Moodbidri	S N MOOBBIDRI POLYTECHNIC ♦ Lecturer

ADDITIONAL RESPONSIBILITIES

- Member of Board of Studies at SJEC, mangaluru.
- Member of Quality and Governance Group at SJEC, Mangaluru.
- VTU Valuation Coordinator at SDIT, Mangalore
- Actively involved in the setting of new laboratories and preparation of lab manuals at SDIT
- Worked as Project Coordinator, Student Mentor., Time-table Coordinator at SDIT.
- Question paper setter for VTU and Deemed Universities.
- Convener – “SHREE DEVI SAMBHRAM” A National level Inter College Technical and Cultural Fest at SDIT, Mangalore
- Convener – “SURABHI” Intra college cultural fest at SDIT, Mangalore.

RELEVANT SKILLS

- **Technical Skills:** VHDL, Verilog.
 - **Software:** MATLAB, Simulink, SPICE, Xilinx, Mentor Graphics.
 - **Programming Languages:** C, Python, R programming.
 - **Skill Sets:** Leadership, Team work, Time Management, Adaptability, Work Ethics
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U G SUBJECT TAUGHT

Sensors and Instrumentation, Basic Electrical Engineering, Digital System Design, Analog Electronic Circuits, Control Systems, Information Theory and Coding, Power Electronics, VLSI Design, Embedded Systems , Microprocessor, Electrical Power Generation, Industrial Electronics.

Signals & Systems Lab, Digital Signal Processing Lab, Digital System Design Lab, Power Electronics Lab, Analog Electronics Lab, HDL Lab, VLSI Lab, Microprocessor Lab. Industrial Electronics Lab.

REFERENCES

Dr. Rohan Pinto

Professor & Controller of Examination

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